Layer	Name	Material	Thickness	Constant	Board Layer Stack	Board Layer Stack
1	Top Overlay					
2	Top Solder	Solder Resist	0.40mil	3.5		
3	Layer 1 - Top Layer	Copper	1.40mil			
4	Dielectric1	370HR	59.20mil	4.8		
5	Layer 2 - Bottom Layer	Copper	1.40mil			
6	Bottom Solder	Solder Resist	0.40mil	3.5		
7	Bottom Overlay					

DESIGN INFORMATION

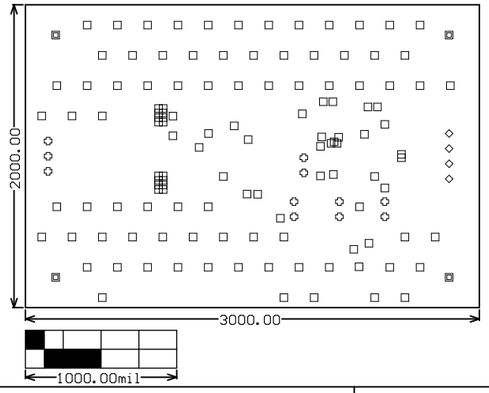
MIN. TRACK WIDTH: 6 MIL
MIN. CLEARANCE: 6 MIL
MIN. VIA PAD SIZE: 20 MIL
MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL
PER IPC-D-275 CLASS 2 LEVEL C
REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL
HOLE SIZE TOLERANCE (UNLESS OTHERWISE SPECIFIED): +/- 3 MIL

MATERIAL:
[ ] FR-408 [ ] FR-4 High Tg [X] OTHER 370HR
THICKNESS: [X] 62 MIL (1.6mm) +/-10% [ ] OTHER
TOLERANCE: [X] ANSI IPC-6012 TYPE 3 CLASS 2
[ ] OTHER +/-
BOW & TWIST: [X] ANSI IPC-6012 TYPE 3 CLASS 2
[ ] OTHER +/-

DRILLING:
REFERENCE: [X] AS SHOWN [X] NC_DRILL FILES
PTH MIN COPPER THICKNESS: [X] 1MIL [ ] OTHER

BOARD FINISH:
SILKSCREEN: [X] TOP [ ] BOTTOM
SILKSCREEN COLOR: [X] WHITE [ ] OTHER
SOLDER RESIST COLOR: [X] GREEN [ ] OTHER
[ ] MATTE [X] SEMI-GLOSS
SURFACE FINISH: [X] IMMERSION GOLD (ENIG) [ ] ENEPIG
[ ] IMM. TIN/SILVER OR EQUIV [ ] OTHER
ARRAY/PANEL: [ ] CUT AND TRIM PER M1 BOARD OUTLINE
[ ] N.C. ROUTE [X] V. SCORE
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBS TO MEET OR EXCEED THE REQUIREMENTS OF:
[X] ANSI IPC-A-600F CLASS -> [ ] 1 [X] 2 [ ] 3
[X] RoHS [ ] OTHER PER ORDER
ALL BOARDS MUST MEET OR EXCEED UL94-V0 REQUIREMENTS.
PCB MUST BEAR THE UL94V-0 UL REGISTERED MATERIAL ID NUMBER

ADDITIONAL REQUIREMENTS:
MICROSECTION: [ ] YES
BARE BOARD ELEC. TEST: [ ] NONE [X] REQUIRED [ ] PER ORDER



Symbol	Quantity	Finished Hole Size	Plated	Hole Type
□	122	10.00mil (0.254mm)	PTH	Round
✪	11	39.37mil (1.000mm)	PTH	Round
◇	4	43.31mil (1.100mm)	PTH	Round
■	4	125.98mil (3.200mm)	PTH	Round
	141 Total			

TEXAS INSTRUMENTS

PROJECT TITLE: ISBA-EVM-01065
DESIGNED FOR: Public Release
FILE NAME: TIDA-01065 SSR_TID.PcbDoc

ALL ARTWORK VIEWED FROM TOP SIDE
LAYER NAME = [illegible]ions
PLOT NAME = Fabrication Drawing

BOARD #: ISA-EVM-01065 REV: Rev A
TID #: TIDA-01065
GENERATED : 8/25/2016 7:02:39 PM

SVN REV: Not In VersionControl
TEXAS INSTRUMENTS

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ENGINEER: Tattiana Davenport
LAYOUT BY: JMM Krypton solutions
SCALE: 1.00
ALTIUM DESIGNER VERSION: 16.0.9.368